

Modeling Analysis Computations and End-to-end Simulation-analysis Workflows

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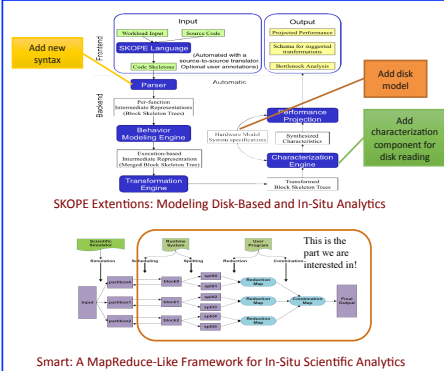
MOTIVATION

- Simulations and experiments are costly to perform, in terms of procuring the required resources, and job wait times.
- Analysis of petabytes of data from simulations and experiments require strategically utilizing the analysis resources.
- Modeling the performance of analysis, simulation-analysis workflows and experiment-analysis workflows helps scientists plan their simulation and experiment campaigns.
- Scientific experiments at synchrotron light sources are configuration-sensitive and depend on many parameters such as the number of projections and dose exposure time. Timely analysis of collected data can help deciding on configuration parameters and lead to more accurate data acquisition.
- Available compute resources at light sources are typically insufficient for analyzing the generated data, thus we require performance models and software tools to perform efficient analysis on distributed HPC resources.

CONTRIBUTIONS

- Developed
 - Performance models for mapReduce-like in situ data analysis jobs
 - In situ and co-analysis execution workflow optimization models for scientific applications
 - Performance models for light source experimental data analysis workflow

MODELING IN SITU MAPREDUCE-LIKE ANALYSIS JOBS



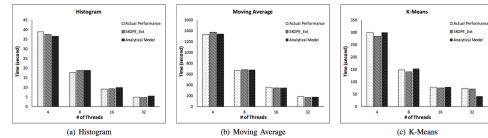
Model for memory access time of 2-level cache hierarchy: $T_{mem} = N_{mem} * (L1_{hit} + P_{L1} * miss * (L2_{hit} + P_{L2} * miss * L_{mem}))$

For applications used in our experiments, the cache miss rate is dependent on dataset size (D), data access stride(s), cache capacity (C1 and C2 for L1 and L2 respectively) and block size (B1 and B2 for L1 and L2 respectively)

Scenario	Array Size	Stride	Frequency of L1 Misses	Miss Rate of L1	Frequency of L2 Misses	Miss Rate of L2
1	$1 < D \leq C_1$	$1 \leq s \leq D/2$	no misses	0	no misses	0
2.a	$C_1 < D \leq C_2$	$1 \leq s \leq B_1$	one miss for every B_1/s elements	s/B_1	no misses	0
2.b	$C_1 < D \leq C_2$	$B_1 < s \leq B_2$	one miss for every element	1	no misses	0
2.c	$C_1 < D \leq C_2$	$B_2 < s \leq D/2$	one miss for every element	1	no misses	0
3.a	$C_2 < D$	$1 \leq s \leq B_1$	one miss for every B_1/s elements	s/B_1	one miss for every B_2/s elements	B_1/B_2
3.b	$C_2 < D$	$B_1 < s \leq B_2$	one miss for every element	1	one miss for every B_2/s elements	s/B_2
3.c	$C_2 < D$	$B_2 < s \leq D/2$	one miss for every element	1	one miss for every element	1

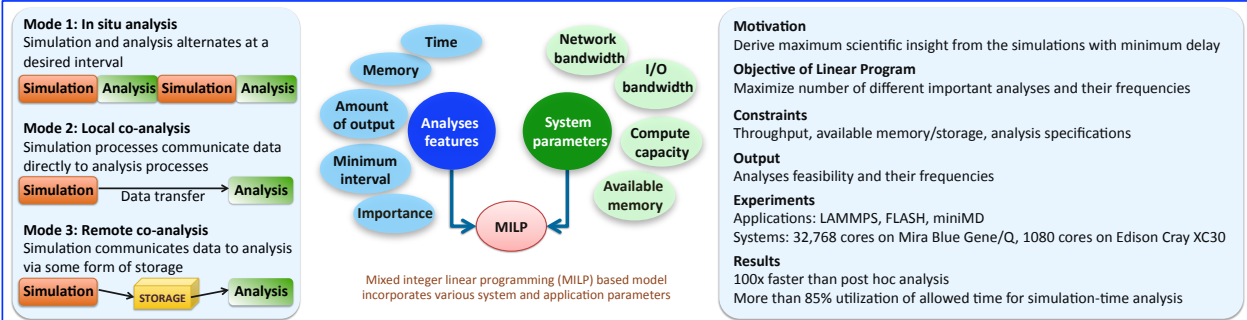
Cache Performance Model for a Two-Level Cache Hierarchy

By defining parameters in the hardware model in SKOPE, and collecting memory access information such as read, write, access stride from code skeletons, we can utilize SKOPE to predict cache performance.

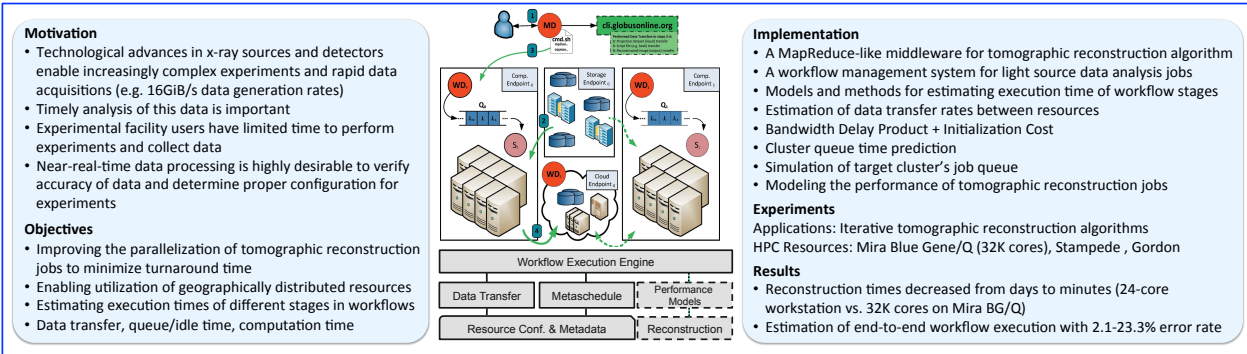


Predicting Scalability of Smart (conducted on the TACC Stampede cluster using 4 nodes)

MODELING END-TO-END SIMULATION-ANALYSIS WORKFLOW



MODELING LIGHT SOURCE DATA ANALYSIS WORKFLOW



CONCLUSIONS

- Modeled performance of map-reduce like in situ data analysis kernels
- Extended SKOPE performance modeling framework to model cache
- Developed models to propose the optimal set of analysis computations that can be performed within time and space constraints
- Proposed mixed-integer linear programs for formulating optimal in situ and co-analysis execution workflows
- Shorter turnaround time of experimental data analysis workflow help verification of collected data and steering experiments at light sources
- Developed performance models and software tools for execution of light source data analysis tasks on distributed HPC resources

REFERENCES

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