Pipelining/Overlapping Data Transfer for Distributed Data-Intensive Job Execution

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Abstract—Scientific workflows are increasingly gaining attention as both data and compute resources are getting bigger, heterogeneous, and distributed. Many scientific workflows are both compute intensive and data intensive and use distributed resources. This situation poses significant challenges in terms of real-time remote analysis and dissemination of massive datasets to scientists across the community. These challenges will be exacerbated in the exascale era.

Parallel jobs in scientific workflows are common, and such parallelism can be exploited by scheduling parallel jobs among multiple execution sites for enhanced performance. Previous scheduling algorithms such as heterogeneous earliest finish time (HEFT) did not focus on scheduling thousands of jobs often seen in contemporary applications. Some techniques, such as task clustering, have been proposed to reduce the overhead of scheduling a large number of jobs. However, scheduling massively parallel jobs in distributed environments poses new challenges as data movement becomes a nontrivial factor.

We propose efficient parallel execution models through pipelined execution of data transfer, incorporating network bandwidth and reserved resources at an execution site. We formally analyze those models and suggest the best model with an improved exploitation of parallelism.

1) Review and theoretical analysis of various pipelined execution models for wide-area distributed computing. They were originally designed for tightly coupled architectures such as vector and superscalar machines.
2) Decompose traditional staging and execution cycles of application tasks into distinct jobs to exploit parallelism by overlapping those cycles in a pipeline.
3) Implement and test the above method via Swift parallel scripting framework [7] on wide-area distributed resources enabled with a third-party GridFTP data transfers.

The remainder of the paper is structured as follows. In Section II, we briefly present background on distributed parallel computing. In Section III, we propose several parallel job execution models and present mathematical analysis of those models and corresponding optimal degrees of parallelism. In Section IV, we evaluate our analytical models with actual implementation in the Swift parallel scripting paradigm with
third-party GridFTP transfers for data staging. In Section V, we briefly summarize our work.

II. BACKGROUND

In this section, we briefly present parallelizing techniques in the literature.

A. Parallel Computing in Local Machines

Parallel computing on single, large machines has been an active research area since the 1990s [8]. Computer architectures can be categorized into four areas: (1) temporal parallelism: pipelining, (2) data parallelism: SIMD, (3) functional parallelism: MIMD, and (4) instruction level parallelism: superscalar, VLIW. In case of applications, several parallel programming models, such as the message-passing model implemented and popularized by MPI and OpenMP standards, have become standard [9].

In order to apply these techniques for distributed applications, additional constraints such as wide-area network communications among distributed processes should be considered.

B. Distributed Parallel Computing

Distributed computing platforms are deployed to run and manage scientific workflows efficiently. Popular workflow management systems include Condor DAGMan [10], Pegasus [11], Taverna [12], and makeflow [13]. Regarding massively parallel job scheduling, Pegasus [11] provides a task clustering technique that groups parallel jobs based on fixed parameters such as number of groups. This static approach has limitations such as ignorance of job properties (e.g., CPU-intensive job). Some approaches have attempted to address the limitations through dynamic task clustering. One example is the Falkon project [14], which dynamically generates workflows and clusters tasks for compute resource sites.

As connection-oriented networks such as ESnet [15] and Internet2 [16] started to provide in-advance network path reservation through OSCARS [17] and as the amount of data is expected to grow rapidly in the near future, data movement and placement become important factors in workflow scheduling algorithms. Accordingly, recent workflow scheduling algorithms focus not only on optimal compute resource allocations but also on efficient data movement [18].

To the best of our knowledge, however, distributed parallel job scheduling has not been tackled in terms of optimization of data transfer. Especially when multiple parallel jobs, each of which needs to receive/send data to/from remote sites, are mapped to a single site, overlapping and pipelined data transfer can significantly improve overall performance. In the following sections, we propose and analyze parallel execution models enabling data transfer to be overlapped with computation.

III. DISTRIBUTED MASSIVELY PARALLEL JOB SCHEDULING

In this section, we first describe challenges in distributed massively parallel job scheduling. We then formally analyze the parallel execution models and propose optimal co-scheduling algorithms, taking into account both compute and network resources.

A. Problem Statement

Scheduling massively parallel jobs in wide-area, federated distributed high-performance computing (HPC) systems such as XSEDE [19] is not trivial for several reasons. First, distributed HPC systems have diverse capability, and users have different resource allocations on the systems depending on subscribed projects. Moreover, each HPC system is managed by different local resource managers, such as MAUI [20] and SLURM [21], which makes job scheduled on the system pass through different internal scheduling policies. With data-intensive workflows, the time required for data movement for jobs becomes more important compared with the CPU time required for running the jobs. Data movement could involve network topologies in a single or multiple domains, and concurrent data movements should be carefully coordinated because network bottlenecks are largely influenced by network topologies. The network resource scheduling efforts may be quite different depending on network properties, packet-oriented vs. circuit-oriented networks.

In the following sections, we first formalize the scheduling problem for a single HPC site where a user has a limited amount of allocation and multiple parallel jobs are given. We show that the solution to the problem is the optimal degree of parallelism, which means how many jobs should be executed in parallel considering compute and network resources. In future, we will extend the scheduling solution to a workflow consisting of many jobs targeted to multiple compute resource sites.

B. Theoretical Analysis of Execution Time

In this section, we theoretically analyze the performance of distributed parallel tasks. We start by describing basic well-known performance equations at the computer instruction level [8]. We then develop analytical performance equations for distributed parallel tasks.

In Equation 1, \( T_{inst}(m, p) \) is the number of base cycles to complete tasks in the instruction level when all instructions take one base cycle, \( k \) is the length of one pipeline iteration, \( m \) is the number of simultaneous instruction issues, \( p \) is the degree of parallelism in each pipeline stage, and \( n \) is the number of iterations. The parameter \( p \) means that each instruction can be further pipelined into \( p \) subpipeline stages. A machine is called a superscalar machine if \( m > 1 \); a machine is called a superpipelined machine when \( p > 1 \).

\[
T_{inst}(m, p) = k + \frac{n - m}{m \times p}
\]  

\( k \) : Length of one pipeline iteration in base cycles  
\( m \) : Number of simultaneous instruction issues  
\( p \) : Degree of parallelism in each pipeline stage  
\( n \) : Number of iterations
In Equation 2, $S_{\text{inst}}$ is speedup compared with sequential execution that requires $nk$ base cycles.

$$S_{\text{inst}} = \frac{nk}{T_{\text{inst}}(m,p)} \quad (2)$$

At the task level, when instructions are substituted by tasks of variable lengths and $p = 1$, the previous equations can be extended as in Equation 3. If the execution times of pipeline tasks vary, the execution time of one pipeline stage is dominated by the maximum execution time of pipeline tasks. Since $T_{\text{task}}(m,1)$ becomes close to $\frac{nk}{m} \times t_{\text{max}}$ as $n$ increases, the speedup for task-level pipelining can be expressed as in Equation 4 if the times of SI, EX, SO are similar.

$$T_{\text{task}}(m,1) = (k + \frac{n - m}{m}) \times t_{\text{max}} \quad (3)$$

$k$: Length of one pipeline iteration in number of tasks

$m$: Number of simultaneous task issues

$n$: Number of pipeline iterations

$t_{\text{max}}$: Maximum execution time of pipeline tasks.

$$S_{\text{task}} = t_{\text{total}} \times T_{\text{task}}(m,1) \simeq nk \quad (4)$$

$t_{\text{total}}$: Total execution time of tasks without pipelining

Based on Equations 3 and 4, we can formally analyze the performance of jobs dispatched to one compute resource site when data movement and computation are overlapped. For example, if four jobs are scheduled on a cluster $A$ consisting of 2 nodes and each job is required to occupy the whole node, the data for the third and fourth jobs can be transferred while the first and second jobs are running. This approach is implementable by using GridFTP and associated data transfer nodes dedicated for data transfers. The dedicated data transfer node model is used by many compute facilities including the national cyberinfrastructure such as XSEDE and this is also the basic model of Science DMZ [22]. We categorize overlapping of data movement and computation into three classes: simple pipelined execution, multiple control-flow based execution, and superscalar-style execution. In the following, we describe each class in more details.

1) Simple pipelined execution (SPE): Without loss of generality, we can assume that a job is composed of three tasks or stages, stage-in (SI), execution (EX), and stage-out (SO). In the SI stage, the input data for the job is transferred from the precedent job sites. In the EX stage, the job runs with the input data and produces output data for descendent jobs. In the SO stage, the output data are transferred to the descendent job sites or to the submit site if this is the last job in the workflow. The simple pipelined execution is the simplest of parallel execution cases, in which the SI, EX, and SO stages are executed in a pipeline as in Figure 1. The execution time(SPE) and speedup(SPE) for this model are analyzed in Equations 5 and 6, respectively.

$$T_{\text{SPE}}(1,1) = (k + (n - 1)) \times t_{\text{max}} \quad (5)$$

$$S_{\text{SPE}} = \frac{n \times t_{\text{total}}}{T_{\text{SPE}}(1,1)} \simeq k \quad (6)$$

One implementation of this model is that, in the EX stage, the job calls a nonblocking data receive function for input data required by the next job and a nonblocking data send function for output data produced by itself at the start and end of the EX stage, respectively.

2) Multiple control-flow-based execution (MCE): Multiple control-flow-based execution (MCE) is a model of a fixed number of independent control flows/threads executing SI, EX, and SO repeatedly. For instance, Figure 2 shows that four threads are pooled for multiple job execution, and their progress is synchronized at best. They may not be synchronized, however, depending on the operating system’s scheduling policy. The typical example of this model is the Swift parallel scripting paradigm. The job throttle parameter in Swift takes control of the number of concurrent job executions. In the best case, the execution time(MCE) and speedup(MCE) for this model are analyzed in Equations 7 and 8, respectively.

$$T_{\text{MCE}} = (k \cdot \left\lfloor \frac{n}{m} \right\rfloor) \times t_{\text{max}} \quad (7)$$

$$S_{\text{MCE}} = \frac{n \times t_{\text{total}}}{T_{\text{MCE}}} \simeq m \quad (8)$$
Then how do we determine optimal $m$ for the system? There should be enough compute resources to run $m$ EX stages. If not, the time required for $m$ EX stages may be more than $t_{\text{max}}$, which leads to prolonged $T_{\text{MCE}}$. For instance, if two CPU cores are available and three jobs are in the EX stage, two jobs will first run simultaneously and the third job will wait until those jobs are completed. Consequently, the total required time would be two times of the one job execution time.

3) Superscalar-style execution (SSE): The superscalar-type execution (SSE) model is a hybrid of the SPE model and the MCE model, in which $m$ jobs are in the same stage and each stage is overlapped with other stages. Figure 3 shows an example of SSE, and equations 9 and 10 are the required time(SSE) and speedup(SSE), respectively, for the SSE model. In this model, $m$ is determined in the same way as in MCE model.

![Fig. 3. Superscalar-style execution.](image)

$$T_{\text{SSE}}(m, 1) = (k + \frac{n - m}{m}) \times t_{\text{max}}$$

$$S_{\text{SSE}} = \frac{n \times t_{\text{total}}}{T(m, 1)} \simeq m \times \frac{t_{\text{total}}}{t_{\text{max}}} \simeq mk$$

C. Coscheduling with networks

When one compute resource has $r$ computing nodes and unlimited I/O capacity is assumed, the model for the best performance is a SSE model with $m = r$ whose speedup are $3r$ and 3 compared with the sequential execution model and the MCE model, respectively. However, the network bandwidth is not unlimited, and the times for the SI and SO stages vary according to the network status and $m$. Therefore, we define the time required for pipeline stages as functions of $m$, where $m = 0$ means a sequential execution. Table I shows a list of time functions for pipeline execution.

With the newly defined functions, Equation 10 can be rewritten as Equation 11.

$$S_{\text{SSE}} \simeq m \times \frac{t_{\text{total}}(0)}{t_{\text{max}}(m)}$$

$$= m \times \frac{t_{SI}(0) + t_{EX}(0) + t_{SO}(0)}{\max\{t_{SI}(m), t_{EX}(m), t_{SO}(m)\}}$$

Table I - Time Function List

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$t_{SI}(m)$</td>
<td>Time required for the SI stage</td>
</tr>
<tr>
<td>$t_{EX}(m)$</td>
<td>Time required for the EX stage</td>
</tr>
<tr>
<td>$t_{SO}(m)$</td>
<td>Time required for the SO stage</td>
</tr>
<tr>
<td>$t_{total}(m)$</td>
<td>Time required for all sequential pipeline stages; $t_{SI}(m) + t_{EX}(m) + t_{SO}(m)$</td>
</tr>
<tr>
<td>$t_{\text{max}}(m)$</td>
<td>Maximum of $t_{SI}(m)$, $t_{EX}(m)$, and $t_{SO}(m)$</td>
</tr>
</tbody>
</table>

For simplicity, we assume that the network bandwidth, $B_{\text{shared}}$, is shared by both the SI and SO stages and that the EX stage is not interfered with the SI/SO stages. In reality, the problem may get more complicated when a complex network topology is involved and network paths used for SI and SO may or may not overlap at our discretion.

![Fig. 4. (a) $t_{SI/\text{SO}}(m)$ plot, (b) $t_{EX}(m)$ plot](image)

With Equation 11, our goal is to find the optimal $m$ that maximizes the speedup $S_{\text{SSE}}$. Since the SI and SO stages are overlapped and the $B_{\text{shared}}$ bandwidth is also shared by both data transfers, Equation 12 holds true.

$$t_{SI}(m) = t_{SO}(m) = m \times \frac{(D_{SI} + D_{SO})}{B_{\text{shared}}}$$

$D_{SI}$: Amount of data transferred in SI stages

$D_{SO}$: Amount of data transferred in SO stages

Therefore, we can substitute $\max\{t_{SI}(m), t_{EX}(m), t_{SO}(m)\}$ by $\max\{m \times \frac{(D_{SI} + D_{SO})}{B_{\text{shared}}}, t_{EX}(m)\}$. The resulting equation is Equation 13.

$$S_{\text{SSE}} = m \times \frac{t_{\text{total}}(0)}{\max\{m \times \frac{(D_{SI} + D_{SO})}{B_{\text{shared}}}, t_{EX}(m)\}}$$

Equation 13 can be further analyzed through the denominator max function. Figure 4 shows the plots of two terms, $m \times \frac{(D_{SI} + D_{SO})}{B_{\text{shared}}}$ ($= t_{SI/\text{SO}}(m)$) and $t_{EX}(m)$, in the max function.

Figure 5 shows the possible four cases of the max function depending on the relative positions of two functions, $t_{SI/\text{SO}}(m)$ and $t_{EX}(m)$. We can find optimal $m$ as follows.

a) Data-intensive case ($\frac{(D_{SI} + D_{SO})}{B_{\text{shared}}}$ > $t_{EX}(1)$): The speedup of this case is constant regardless of $m$; we can pick
m = 1, which requires the simplest implementation.

\[
\max \{ \cdot \} = m \times \frac{D_{SI} + D_{SO}}{B_{shared}}, m > 0
\]

\[
S_{SSE} = \frac{t_{total}(0) \times B_{shared}}{D_{SI} + D_{SO}}
\]

\[
\text{Optimal } m = 1
\] (14)

b) Compute and data-intensive case \( \left( \frac{D_{SI} + D_{SO}}{B_{shared}} > \frac{t_{EX}(1)}{r} \right) \land (t_{SI/SO}(1) < t_{EX}(1)) \equiv (t_{EX}(1) < \frac{D_{SI} + D_{SO}}{B_{shared}} < t_{EX}(1)): \) This case is further divided into two subcases: prior and posterior to the intersection of \( t_{SI/SO}(m) \) and \( t_{EX}(m) \). Optimal \( m \)'s of both cases are same.

\[
\max \{ \cdot \} = \begin{cases} t_{EX}(1), & \text{if } 1 < m \leq \frac{t_{EX}(1) \times B_{shared}}{D_{SI} + D_{SO}} \leq m < r \ \frac{D_{SI} + D_{SO} \times m}{B_{shared}}, & \text{if } \frac{t_{EX}(1) \times B_{shared}}{D_{SI} + D_{SO}} \leq m \leq r \end{cases}
\]

\[
S_{SSE} = \begin{cases} \frac{t_{EX}(1) \times t_{total}(0)}{D_{SI} + D_{SO}}, & \text{if } 1 < m \leq \frac{t_{EX}(1) \times B_{shared}}{D_{SI} + D_{SO}} \leq m < r \ \frac{t_{total}(0)}{B_{shared} \times t_{total}(0)}, & \text{if } \frac{t_{EX}(1) \times B_{shared}}{D_{SI} + D_{SO}} \leq m \leq r \end{cases}
\]

Optimal \( m = \frac{t_{EX}(1) \times B_{shared}}{D_{SI} + D_{SO}} \) (15)

\[c) \text{ Impossible case } \left( \frac{D_{SI} + D_{SO}}{B_{shared}} < \frac{t_{EX}(1)}{r} \right) \land (t_{SI/SO}(1) > t_{EX}(1)) \equiv (t_{EX}(1) < \frac{D_{SI} + D_{SO}}{B_{shared}} < t_{EX}(1)): This is an impossible case.\]

\[d) \text{ Compute-intensive case } \left( \frac{D_{SI} + D_{SO}}{B_{shared}} < \frac{t_{EX}(1)}{r} \right) \land (t_{SI/SO}(1) < t_{EX}(1)) \equiv (\frac{D_{SI} + D_{SO}}{B_{shared}} < \frac{t_{EX}(1)}{r}): \text{ The speedup of this case increases as } m \text{ grows. However, } m \text{ is limited by } \lceil n/3 \rceil, \text{ which guarantees pipelined execution at least.}\]

\[\text{Fig. 5. (a) Data-intensive case (b) Compute and data-intensive case (c) Impossible case (d) Compute-intensive case.}\]

\[\text{Fig. 6. A data-flow view of the pipeline; the jobs are shown in boxes and data dependencies as arrows, the jobs in non-connected boxes are independent and can be executed in parallel.}\]

\[
\max \{ \cdot \} \simeq \frac{(m + n - 1)t_{EX}(1)}{r}, m > 0
\]

\[
S_{SSE} = \frac{mm \times t_{total}(0)}{(m + n - 1)t_{EX}(1)}
\]

\[\text{Optimal } m = \lceil n/3 \rceil, \text{ where } n \text{ is the number of jobs. (16)}\]

IV. EXPERIMENTAL EVALUATIONS

An alternative, data-flow view of the pipeline shown in figure 1 can be as shown in figure 6. Jobs are shown as boxes and arrows between boxes show a data dependency between these jobs. Each triplet of SI-EX-SO forms one logical task. This view of decomposed tasks into three jobs gives a maximum opportunity for parallel and overlapped execution assuming each task in this pipeline are independent, either by the virtue of data parallelism or workflow parallelism discussed in Section I.

The data-flow view depicted in Figure 6 above is implemented by coding it using the Swift parallel scripting frame-
work. The programming model of Swift allows for implicit parallelism governed and steered via data-flow constraints. The underlying Karajan [23] engine and the Coasters [24] framework of Swift provides a built-in data staging mechanism using socket-based TCP transfers. However, in the current implementation, we bypass the builtin staging for third-party data transfer via GridFTP motivated by the following factors:

1) GridFTP servers used for third-party data staging are performant and dedicated for data movement resulting in faster data transfers compared to regular TCP channels.

2) Detaching the data staging steps from computation allows for overlapped data transfer and execution which in turn improves the overall execution time of application.

A Swift script implementation for the pipeline is shown in listing 1. Swift variables and function definitions are defined in the 'imported' header (line 1) file. The first (lines 6, 9-10) and last two (lines 19-20, 23) stages of the pipeline are represented by the lines before and after the foreach loop. The foreach (lines 12-16) loop represents the jobs for each of the task. A ‘token’ variable is used for synchronization between stage_in, exec and stage_out jobs. Array indices makes sure that the same tasks are chained by the dependencies while still running in fully asynchronous execution mode.

The script shown in the listing 1 was executed from the login node of the Stampede supercomputer located at Texas. The data was hosted on the data storage node of remotely located San Diego Supercomputer Center’s (SDSC) Trestles system. A complete setup is shown in figure 7. Figure 8 shows performance results for our approach with different degrees of parallelism (controlled via Swift throttle parameter) and an increasing number of data sizes. The execution time of the jobs were adjusted in proportion to the average data movement times from the staging jobs. The plot shows resulting execution times averaged over 5 runs with standard deviation recorded. A basic run with sequential pipeline execution shows a baseline result as shown by the blue (solid) plot. As expected, the performance is poor since each task is run in three sequential stages. A clearly improved performance is seen when running the jobs in parallel pipelined fashion as shown by the green (dashed) plot corresponding to the MCE model with $m = 3$, a parallelism of 3 jobs at a time. Finally, as seen by the red (dotted) plot corresponding to the SSE model with $m = 3$, an improvement by 50% is observed when the parallelism is increased to 9 mimicking a superscalar pipeline execution. In our experiments, the results follows the models in Section III-B where the data transfer performance is linearly scalable as the number of data streams increases. The MCE model with $m = 3$ is about 2.4 times faster than the sequential model, which is comparable to the ideal speedup with linearly scalable data transfer rates is 3. We suspect that this is either because a single data transfer is not saturating the network link or multiple alternate network paths exist between two hosts. In former case, we will tune data transfers to evaluate models taking into account network contention in Section III-C. In latter case, we will investigate more sophisticated parallel execution models to incorporated multiple network paths.

V. CONCLUSION

We propose efficient parallel execution models through pipelined execution of data transfer, incorporating network
bandwidth and reserved resources at an execution site. We formally analyze those models and suggest the best model with the optimal degree of parallelism. We also implement our model in the Swift parallel scripting paradigm using GridFTP. Experiments on real distributed computing resources show that our model with optimal degrees of parallelism outperform the current parallel execution model by as much as 50% reduction of total execution time.

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